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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is

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/Mark D. Saralino/

March 4, 2008

Date

Mark D. Saralino

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Moriyoshi NAKASHIMA et al.

Application No.: 10/595,424

Filing Date: April 18, 2006

For: SEMICONDUCTOR CHIP MOUNTED INTERPOSER,

SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP

INTERPOSER FABRICATION METHOD, BARE CHIP MOUNTED

INTERPOSER, AND INTERPOSER SHEET

Examiner: Alexander O. Williams

Art Unit: 2826

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION DATED FEBRUARY 8, 2008

Sir:

Favorable reconsideration of the above-identified application is respectfully requested in view of the following amendments and comments.

Application No. 10/595,424

Claims 1, 7, 8 and 10-12 are pending in the application and are subject to restriction. Applicants hereby elect Group I (claims 1, 7, 11 and 12) without traverse. Substantive examination of claims 1, 7, 11 and 12 is respectfully requested.

Should the Examiner feel that a telephone interview would be helpful to facilitate favorable prosecution of the above-identified application, the Examiner is invited to contact the undersigned at the telephone number provided below.

Should a petition for an extension of time be necessary for the timely reply to the outstanding Office Action (or if such a petition has been made and an additional extension is necessary), petition is hereby made and the Commissioner is authorized to charge any fees (including additional claim fees) to Deposit Account No. 18-0988.

Respectfully submitted,

RENNER, OTTO, BOISSELLE & SKLAR, LLP

/Mark D. Saralino/

Mark D. Saralino Reg. No. 34,243

DATE: March 4, 2008

The Keith Building 1621 Euclid Avenue Nineteenth Floor Cleveland, Ohio 44115 (216) 621-1113